

PATENT 0941-0835P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: KER, Ming-Dou et al. Conf.:

3912

Appl. No.:

10/658,772

Group: 2815

Filed:

September 10, 2003 Examiner: A.R. Wilson

For:

METHOD OF MANUFACTURING AN ESD

PROTECTION DEVICE WITH THE SAME MASK FOR

BOTH LDD AND ESD IMPLANTATION

LARGE ENTITY TRANSMITTAL FORM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 December 9, 2004

Sir:

Transmitted herewith is a Reply to Restriction/Election

Requ.	rement in the above-identified application.
	The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
	Petition for $()$ month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). §0.00 for the extension of time.
\boxtimes	No fee is required.
	A check in the amount of \$0.00 is enclosed.
	Please charge Deposit Account No. 02-2448 in the amount of \$0.00. A triplicate copy of this sheet is attached

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

Falls Church, VA 22040 0747

BIRCH, STEWART, KOLASCH & BIRCH, LLP

Joe McKinney N

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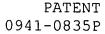
(703) 205-8000

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KM/asc 0941-0835P

Attachment(s)

2





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Applicant: KER, Ming-Dou et al. Conf.: 3912

Appl. No.: 10/658,772 Group: 2815

Filed: September 10, 2003 Examiner: Allan R. Wilson

For: METHOD OF MANUFACTURING AN ESD

PROTECTION DEVICE WITH THE SAME MASK FOR BOTH LDD AND ESD IMPLANTATION

REPLY TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

December 9, 2004

Sir:

semiconductor device.

Responsive to the Office Action dated November 9, 2004, the following election and remarks are respectfully submitted in connection with the above-identified application.

REMARKS

Claims 1-20 are now pending in the present application.

The Examiner has given the following Restriction Requirement:

Group I, claims 11-20, drawn to a semiconductor device; and

Group II, claims 1-10, drawn to a method of making a

In response to this requirement, Applicants hereby elect Group I, claims 11-20 drawn to a semiconductor device. This election is with traverse.